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				Application Number	
				Filing Date	
				First Named Inventor	Chambers et al.
				Group Art Unit	
Examiner Name					
Sheet	1	of	1	Attorney Docket No.	TI-36031

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
<i>De</i>	AA	5,804,848		Mukai	09/08/1998	Entire Document
	AB	5,899,710		Mukai	05/04/1999	Entire Document
	AC	2003/0136963	A1	Krivokapic et al.	07/24/2003	Entire Document
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FOREIGN PATENT DOCUMENTS								
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<i>De</i>	CA	"25nm CMOS Omega FETs", IEEE, FU-LIANG YANG, HAO-YU CHEN, FANG-CHENG CHEN, CHANG-CHUAN HUANG, CHANG-YUN CHANG, HSIEN-KRANG CHIU, CHI-CHUANG LEE, CHI-CHUN CHEN, HUAN-TSUANG HUANG, CHIH-JIAN CHEN HUN-JAN TAO, YEE-CHIA YEO, MONG-SONG LIANG and CHENMING HU. 2002. 4 pgs.					
	CB	"SOI Devices for Sub-0.1 μ m Gate Lengths", PROC. 23 rd International Conference on Microelectronics (Miel 2002), Vol 1, NIS, Yugoslavia, May 12-15, 2002, J.P. COLINGE, J.T. PARK and C.A. COLINGE, pp. 109-113.					
	CC	"Advanced Depleted-Substrate Transistors: Single-Gate, Double-Gate and Tri-Gate", 2002 International Conference on Solid State Devices and Materials (SSDM 2002, Nagoya, Japan, ROBERT CHAU, BRIAN DOYLE, JACK KAVAILEROS, DOUGLAS BARLAGE, ANAND MURTHY, MARK DOCZY, RAFAEL RIOS, TOM LINTON, REZA ARGHAVANI, BEN JIN, SUMAN DATTA and SCOTT HARELAND, pp.1-23.					
<i>De</i>	CD	"Beyond the Conventional Transistor", IBM J. Res. & Dev., Vol. 46, No. 2/3, March/May 2002, H.-S.P. WONG, PP. 133-168.					
	CE						
	CF						
	CG						
	CH						

Examiner Signature	<i>De</i>	Date Considered	3/2005
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